

ABSTRACT

A 3Tr-operated CMOS solid-state imaging apparatus comprises a plurality of pixels including adjacent first and second pixels **230** and **231** including photodiodes **201** for converting light into signal charges and transfer transistors for reading out the signal charges accumulated in the photodiodes, respectively. The first pixel **230** further includes a reset transistor connected at one end to the photodiodes **201** of the first and second pixels **230** and **231** and supplied at the other end with a power voltage, and the second pixel **231** further includes an amplifier transistor having a gate electrode **204** connected to the transfer transistors of the first and second pixel **230** and **231** and supplied at its drain with a power voltage.